

Abstract

[0054] Transmitter and receiver circuitry for
64b/66b encoding or other similar padded data
signalling. The required transmitter clock circuitry
5 is simplified by using one clock signal source as a
basis for at least partly processing the data both
before and after padding. Appropriate frequency
multiplication and division factors are employed to
make this possible. Similar techniques are used in
10 receiver circuitry.